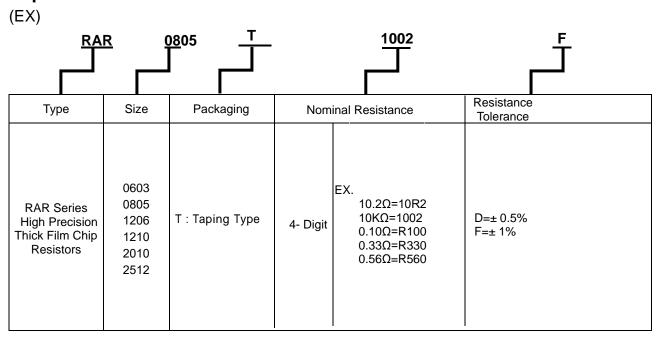


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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of ROHS directive for RAR series high precision thick film chip resistors.
- 1.2 This product is for automotive electronic application.
- 1.3 AEC-Q200 qualified, grade 0.

2 Explanation Of Part Number:



3 General Specifications:

	you or an opposition of the state of the sta						
Туре	Rated Power at 70℃	Max. Working Voltage	Max. Overload Voltage	T.C.R (ppm/℃)	Resistance Range D(±0.5%) \rightarrow F(±1%) E-24 \rightarrow E-96		
RAR (0603)	1 10 W	75V	150V	±50	$100\Omega \le R < 1M\Omega$		
RAR (0805)	W	150V	300V	±50	$100\Omega \le R < 1M\Omega$		
RAR (1206)	W	200V	400V	±50	$100\Omega \le R < 1M\Omega$		
RAR (1210)	W	200V	400V	±50	$100\Omega \le R < 1M\Omega$		
RAR (2010)	-3 -4 W	200V	400V	±50	$100\Omega \le R < 1M\Omega$		
RAR (2512)	1W	200V	400V	±50	$100\Omega \leq R < 1M\Omega$		
C	perating Tem	perature Ran	ge		-55°C ~ +155°C		

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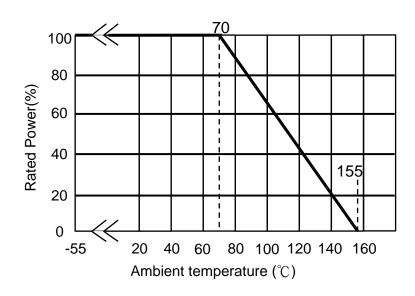


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3.1 Power Derating Curve:

Temperature Range: -55° C ~ $+155^{\circ}$ C

If the ambient temperature exceeds 70 degrees centigrade to 155 degrees centigrade, the power can be modified by the curve as below.



3.2 Voltage Rating or Current Rating:

Rated Voltage: DC voltage or AC voltage (rms) based on the rated power. The voltage can be calculated by the following formula. If the calculated value exceeds the Max. voltage specified in the Table 3, the Max. voltage rating is set as the voltage rating.

$$E = \sqrt{R \times P}$$
 E= Voltage rating (V)
P= Power rating (W)
R= Nominal resistance(Ω)

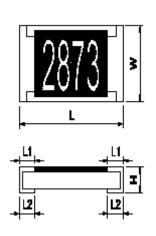
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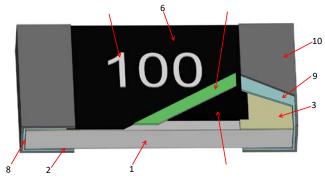
4 Dimensions:

Unit: mm



	Dimension					
		L	W	Н	L1	L2
Туре	Size Code					
RAR	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
RAR	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.20
RAR	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.20
RAR	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RAR	2010	5.00±0.20	2.50±0.20	0.60±0.10	0.65±0.20	0.65±0.20
RAR	2512	6.30±0.20	3.20±0.20	0.60±0.10	0.65±0.20	0.65±0.20

5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating	
2	Bottom inner electrode	7 Marking		
3	Top inner electrode	8	Terminal inner electrode	
4	Resistive layer	9	Ni plating	
5	1st Protective coating	10	Sn plating	

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6 Reliability Test:

rtonasmty		
Item	Conditions	Specifications
		Resistors
High	Put the specimens in the chamber with temperature of 155±3℃ for 1000 hours. Then take them out to stabilize	△R%=±1.0%
	in room temperature for 24±4hr or more, and measure of its resistance variance rate.	
, , ,	Experiment evidence: AEC-Q200	
	Put the specimens in the High & low temperature test chamber with temperature varies from -55°C to 125°C	△R%=±1.0%
	for 15 minutes and total 1000 cycles. Then take them out to stabilize in room temperature for 24±4hr or more, and	
2,4 3	measure of its resistance variance rate. Experiment evidence: AEC-Q200	
	Applied 2.5 times rated voltage for 5 seconds and	△R%=±1.0%
Short Time	release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refers to item 3.	ΔIV/0-±1.070
Overload	general specifications) Refer to JIS-C5201-1 4.13	
		△R%=±2.0% △R%=±2.0%
Operational Life	the chamber with temperature of 125±3°C and load the voltage for 1000 hours. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Note: The input voltage shall refer to the power de-rating curve (referring to page 2,No.3.2) Experiment evidence: AEC-Q200	ΔΛ /0=±2.0 /0
Board Flex (Bending Test)	Solder the specimens on the test PCB and put the PCBA	△R%=±1.0% No mechanical damage, peel-off of side end or chip crack.

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		Charifications
Item	Conditions	Specifications
	The specimens are fully immersed into the Pb-free	Resistors △R%=±1.0%
	solder pot, then take them out to stabilize for 1 hour or	ZI(70=±1.070
Resistance to	more and measure of its resistance variance rate.	
Soldering	Temp of solder pot : 260±5°C	
Heat	Soldering duration: 10±1sec.	
	Experiment evidence AEC-Q200	
	Put the specimens on the test fixture and two	△R%=±3.0%
	(2)discharges (2KVDC) shall be applied to each PUT,	
	one (1) with a positive polarity and one (1) with a	
	negative polarity. Afterwards, the specimens stabilize for	
ESD	30min or more and measure of its resistance variance	
	rate. The test is performed with direct contact and regular discharge mode. The resistor and capacitor used	
	on the spearhead is 2000Ω and 150pF respectively.	
	on the openhed to 200012 and 100pt 100poutery.	
	Experiment evidence AEC-Q200	
	Test method:	1.Soldering coverage over 95%
		2.At the edge of terminal, the object
	Precondition:	underneath (e.g. white ceramic) shall
	The specimens are subjected to 155℃ dry bake for	not expose.
	4hrs±15min.	
	The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of	
	235 \pm 5°C for 5+0/-0.5 sec. Then rinse with water and	
Solderability	observe the soldering coverage under the microscope.	
	bbscrve the soldering coverage under the interescope.	
	Test item 2 (Leaching test): Method D	
	The specimens are immersed into the flux first, then fully	
	immersed into the solder pot, at a temperature of	
	260±5℃ for 30+0/-0.5 sec. Then rinse with water and	
	observe the soldering coverage under the microscope.	
	Experiment evidence AEC-Q200 (R2-R1)	Refer to item 3. General specifications
	$TCR (ppm/^{\circ}C) = \frac{(R2-R1)}{R1(T2-T1)} \times 10^{6}$	ittoro to item 5. General specifications
	R1: Resistance at room temperature (Ω)	
	R2: Resistance at -55°C or +125°C(Ω)	
	T1: Room temperature (°C)	
	T2: Temperature -55°C or +125°C	
	Experiment evidence: AEC-Q200	

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7 Measurement Point:

Measure from bottom electrodes			Unit : mm
A -	DIM TYPE	А	В
	RAR0603	1.35±0.05	0.35±0.05
	RAR0805	1.80±0.05	0.35±0.05
	RAR1206	2.90±0.05	0.35±0.05
○ C	RAR1210	2.90±0.05	0.35±0.05
Current Terminal Transport Trans	RAR2020	4.50±0.05	1.15±0.05
Voltage Terminal	RAR2512	5.90±0.05	1.60±0.05

8 Plating Thickness:

- 8.1 Ni:<u>≥</u>2µm
- 8.2 $Sn(Tin):\geq 3\mu m$ 8.3 Sn(Tin):Matte Sn

9 Rule of package empty quantity:

9.1 Empty quantity for each reel is not allowed to exceed 0.1% of the whole quantity, and continuous 2pcs (included) empty are also unallowed.

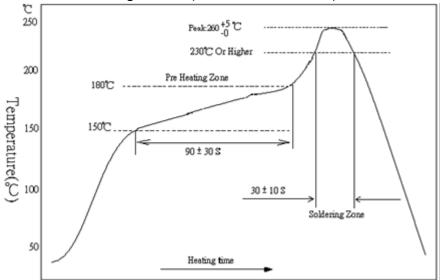
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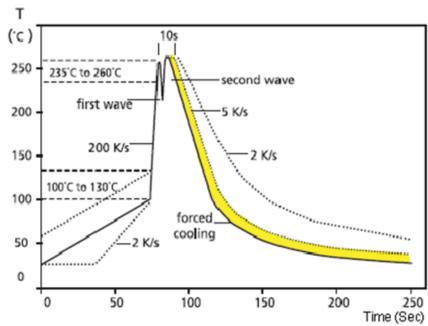
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10 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)

- 10.1 Recommend Soldering Method:
- 10.1.1Lead Free IR Reflow Soldering Profile (MEET J-STD-020D)



Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds 10.1.2Lead Free Double-Wave Soldering Profile. (This applies to 0603 and above size products)



10.1.3Soldering Iron: temperature 350°C±10°C, dwell time shall be less than 3 sec.

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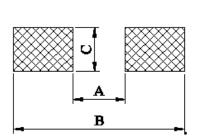


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Unit:mm

10.2 Recommend Land Pattern Design (For Reflow Soldering):

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



TYPE	А	В	С
RAR0603	0.8	2.1	0.9
RAR0805	1.2	3.0	1.3
RAR1206	2.2	4.2	1.6
RAR1210	2.2	4.2	2.8
RAR2010	3.5	6.1	2.8
RAR2512	3.8	8.0	3.5

10.3 Automobile Electronic Application:

This specification is for automobile electronic use. RALEC will take no responsibility if any damage, cost or loss occurs when the product has been used in any special circumstances.

- (a) Information entertainment navigation audio control units.
- (b) Comfortable door, window, seat control unit.
- (c) Internal lighting control unit.

10.4 Environment Precautions:

If consumer intends to use our company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

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10.5 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

10.6 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature
- (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of its fail-safe design to ensure the system safety.

11 Storage and transportation requirement:

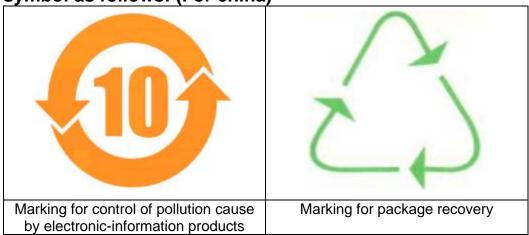
- 11.1 The temperature condition must be controlled as 25±5℃, and the R.H. must be controlled as 60±15%. The stock can maintain quality level in two years.
- 11.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2.
- 11.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

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13 Attachments:

13.1 Document Revise Record (QA-QR-027)

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